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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/768,556	01/25/2001	Yukihiro Inoue	L8462.01101	5136	
75	90 08/09/2002				
STEVENS, DAVIS, MILLER & MOSHER, L.L.P.			EXAMINER		
Suite 850 1615 L Street, N.W.			GEBREMARIAM, SAMUEL A		
Washington, D			ART UNIT	PAPER NUMBER	
			2811		
•			DATE MAILED: 08/09/2002	DATE MAILED: 08/09/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

•		Application No.	Applicant(s)	-4
		09/768,556		
	Office Action Summary	Examiner	INOUE, YUKIHIRO  Art Unit	
		Samuel A Gebremariam	i i	
D	The MAILING DATE of this communication app	ears on the cover sheet with the co	2811	
A SH THE - Exte after - If th - If NO - Failt - Any	MORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. ensions of time may be available under the provisions of 37 CFR 1.13 rs IX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a reply D period for reply is specified above, the maximum statutory period we ure to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	'IS SET TO EXPIRE 3 MONTH( 6(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days till apply and will expire SIX (6) MONTHS from	S) FROM  nely filed s will be considered timely. the mailing date of this communication.	
1)🛛	Responsive to communication(s) filed on 29 M	lay 2002 .		
2a)⊠	This action is <b>FINAL</b> . 2b) ☐ This	s action is non-final.		
3) [	Since this application is in condition for allowar closed in accordance with the practice under E ion of Claims	nce except for formal matters, pr Ex parte Quayle, 1935 C.D. 11, 4	osecution as to the merits is 53 O.G. 213.	;
4) 🖂	Claim(s) $\underline{5-12}$ is/are pending in the application.			
	4a) Of the above claim(s) is/are withdraw	n from consideration.		
	Claim(s) is/are allowed.		)	
6)⊠	Claim(s) <u>5-12</u> is/are rejected.		/	
7)	Claim(s) is/are objected to.			
8)	Claim(s) are subject to restriction and/or	election requirement.	_	
Application	on Papers	·		
1	The specification is objected to by the Examiner.			
10)⊠ ⊺	The drawing(s) filed on is/are: a)☐ accepte	ed or b)⊡ objected to by the Exam	niner.	
	Applicant may not request that any objection to the o	drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).	
11)∟J T	he proposed drawing correction filed oni	s: a)□ approved b)□ disapprov	ed by the Examiner.	
40)[] =	If approved, corrected drawings are required in reply			
	he oath or declaration is objected to by the Exam	niner.	·	
	nder 35 U.S.C. §§ 119 and 120			
13) 📗 📝	Acknowledgment is made of a claim for foreign p	riority under 35 U.S.C. § 119(a)-	(d) or (f).	
a)[	]All b)☐ Some * c)☐ None of:			
1	1. Certified copies of the priority documents h	nave been received.		
2	2. Certified copies of the priority documents h	nave been received in Application	ı No	
3	B. Copies of the certified copies of the priority application from the International Burea et the attached detailed Office action for a list of	documents have been received	in this National Stage	
14)∐ Ac	knowledgment is made of a claim for domestic p	priority under 35 U.S.C. & 119(e)	(to a provisional application)	
a)	☐ The translation of the foreign language provis cknowledgment is made of a claim for domestic p	ional application has been received	hav	1.
Attachment(s	s)	, 20 0.0.0. 33 120 d	14 J. 14 J.	
2) Notice of 3) Informa	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) tion Disclosure Statement(s) (PTO-1449) Paper No(s)	5)   Notice of Informat Dat	PTO-413) Paper No(s) ent Application (PTO-152)	
S. Patent and Trade				

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#### **DETAILED ACTION**

### **Drawings**

Applicant is required to submit a proposed drawing correction in reply to this
 Office action. However, formal correction of the noted defect can be deferred until the application is allowed by the examiner.

Figures 3a-3d should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g).

## Claim Rejections - 35 USC § 112

2. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 9 and 10 recite the limitation "protruding regions" in the claims. There is insufficient antecedent basis for this limitation in the claim.

# Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 5-12, in so far in compliance of 35 U.S.C. 112 are rejected under 35 U.S.C. 103(a) as being unpatentable over Murakami US patent No. 4,819,045 in view Yang US Patent No. 6,306,700.

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Regarding claim 9 Murakami teaches a semiconductor device comprising: a gate insulator film 9 of a transistor formed in a predetermined region on a region of a first conductivity type 3; a gate electrode 11 of the transistor formed on the gate insulator film; a source diffusion layer 5 and a drain diffusion layer 7 of a second conductivity type formed on the region of the first conductivity type; a diffusion layer 15 of the first conductivity type formed so as to surround the gate insulator film 9 so as to be in contact therewith, the diffusion layer of the first conductivity type having a higher impurity concentration than the region of the first conductivity type, and in which regions at both ends, in a direction of a channel width, of the gate insulator film protrude from a boundary, in a lateral direction, wherein the diffusion layer of the first conductivity type is formed so as not to be present below the gate insulator film but to be in contact with the protruding regions at both ends, in the direction of the channel width, of the gate insulator film.

Murakami does not teach a source side offset diffusion layer and the drain side offset diffusion layer of the second conductivity type being present around the source diffusion layer and the drain diffusion layer so as to be in contact therewith, respectively; a diffusion layer of the first conductivity type formed so as to surround the source side offset diffusion layer, the drain side offset diffusion layer and the diffusion layer of the first conductivity type having a higher impurity concentration than the region of the first conductivity type, and in which regions at both ends, in a direction of a channel width, of the gate insulator film protrude from a boundary, in a lateral direction between the source side off set diffusion layer and the drain side of set diffusion layer.

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Yang teaches forming source (222a)/drain (222b) offset structure with the same conductivity as the source (232a)/drain (232b) diffusion layer such that (222a/222b) is lower in impurity concentration than (232a/232b) and formed around the source/drain diffusion layers (fig. 6).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the source/drain offset structure taught by Yang in the structure of Murakami in order to increase the breakdown voltage (col. 4, line 1-9)

Regarding claim 10, Murakami teaches substantially the entire claimed structure of claim 9 above including wherein the diffusion layer of the first conductivity type is formed so as to be separate from the protruding regions at both ends, in the direction of the channel width, of the gate insulator film (fig. 1).

Regarding claims 11 and 12 Murakami teaches substantially the entire claimed structure of claim 9 above including the transistor is a high voltage transistor, the source diffusion layer and the drain diffusion layer are high impurity concentration, and the source side offset diffusion layer and the drain side offset diffusion layer are lower in impurity concentration than the source diffusion layer and the drain diffusion layer (fig. 1).

Regarding claims 5-8, Murakami teaches substantially the entire claimed structure of claim 9 above including the diffusion layer of first conductivity type is a channel stopper region (fig. 1, col. 1, line 51-68).

## Response to Arguments

4. Applicant's arguments with respect to claims 9-12 and 5-8 have been considered but are most in view of the new ground(s) of rejection.

#### Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. References C and D are cited as being related to a semiconductor device.

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Samuel Admassu Gebremariam whose telephone number is 703 305 1913. The examiner can normally be reached on 8:00am-4: 30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (703) 305-7646. The fax phone numbers

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for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Samuel Admassu Gebremariam August 6, 2002 TOM THOMAS
SUPERVISORY PATENT EXAMMER
TECHNOLOGY CENTER 2800